FIG. 1

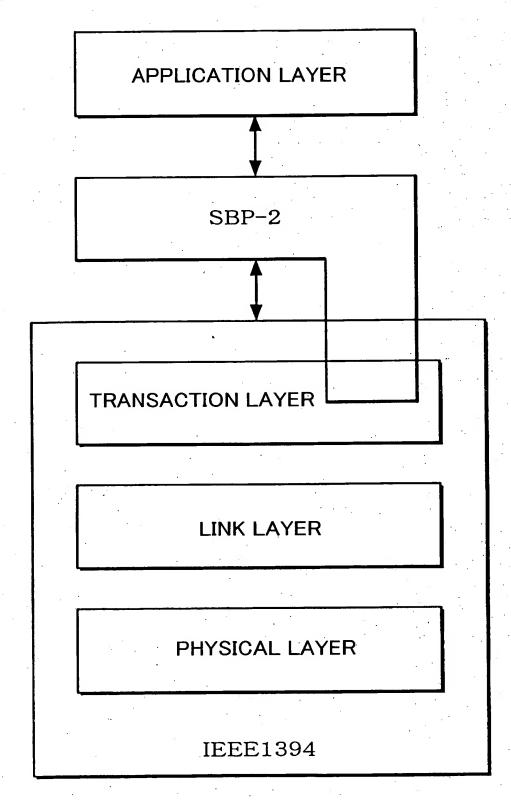
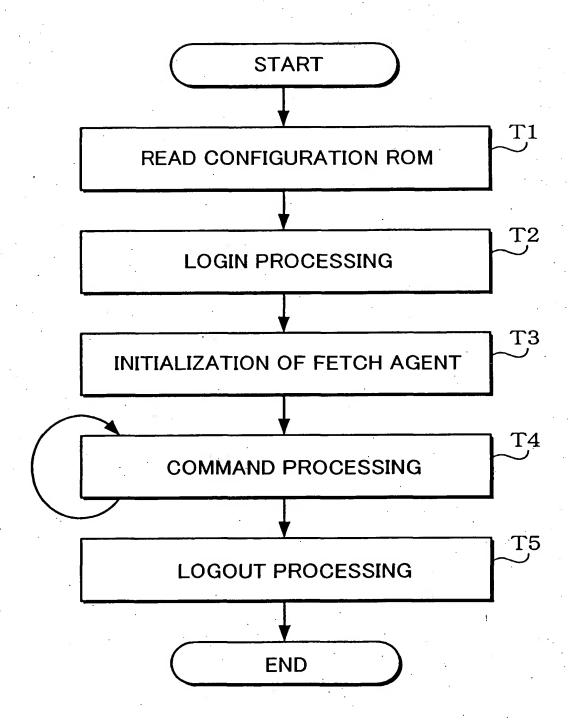


FIG. 2



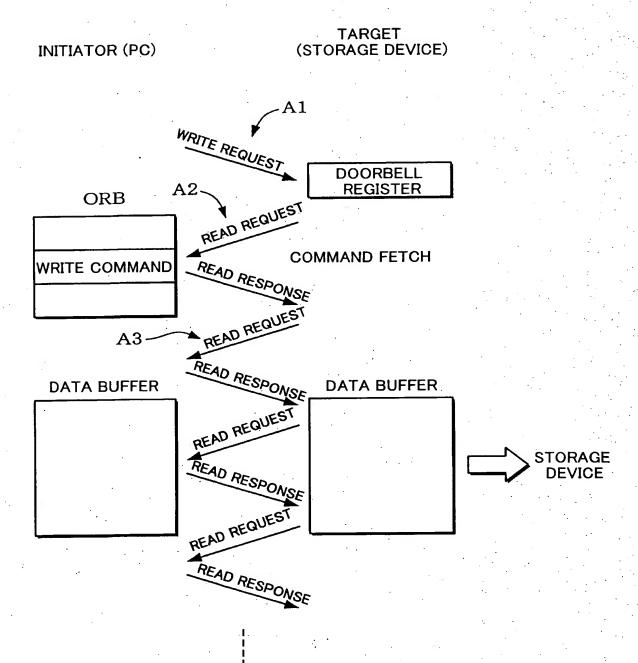
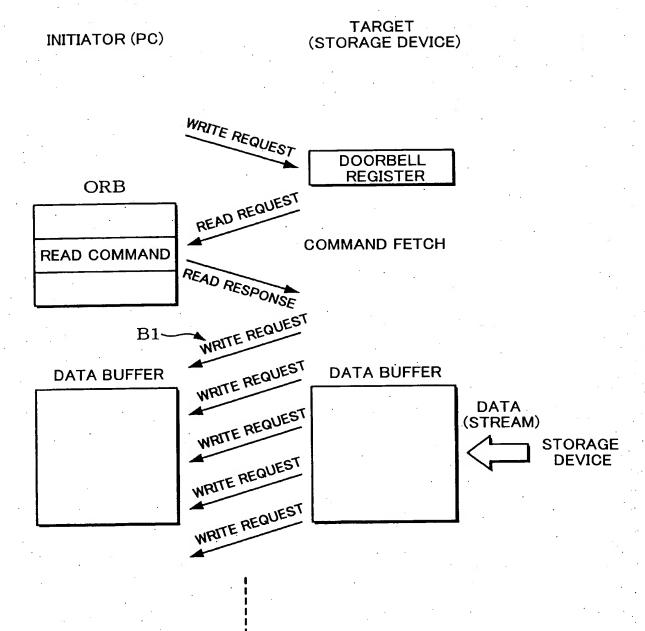


FIG. 4



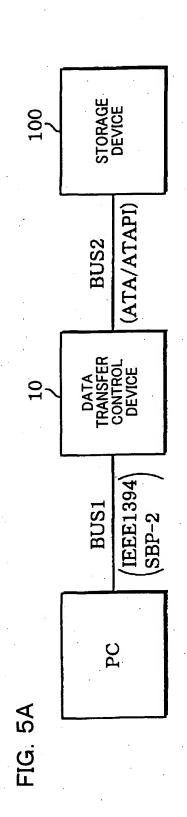
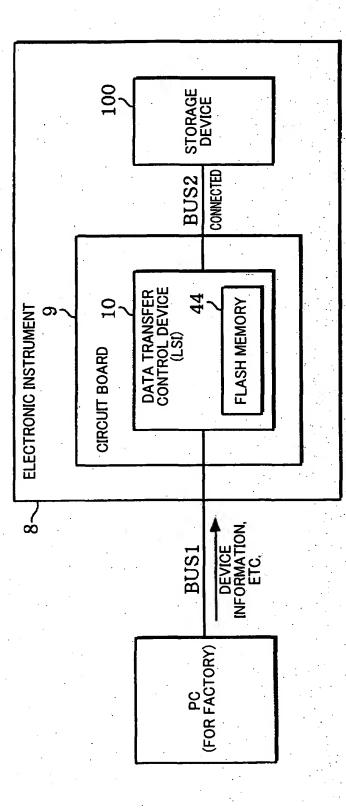
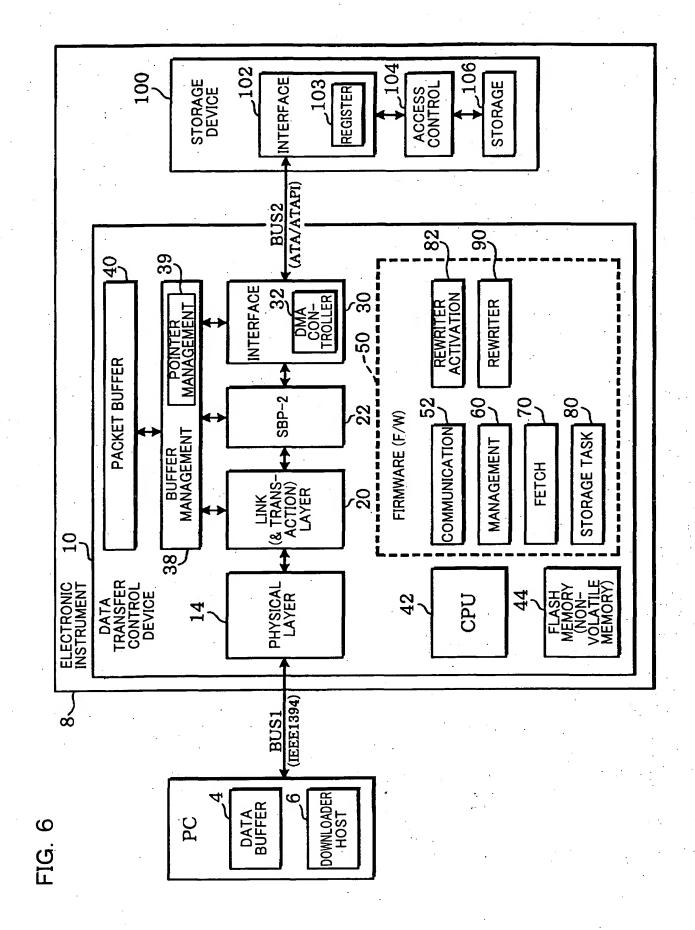


FIG. 5B DURING FABRICATION (DOWNLOAD MODE)





C00000h		·	
	REWRITER INTERRUPT VECTOR TABLE AREA		
	BOOT AREA		
·	REWRITER PROGRAM AREA		
C02000h	REWRITER CONFIG ROM AREA		
Cozooon	SBP-2 FIRMWARE PROGRAM AREA	. •	
	PROGRAM WRITE COMPLETION MARK AREA		
	SBP-2 FIRMWARE CONFIG ROM AREA		REWRITE AREA
	REWRITER ENABLE FLAG (REF) AREA	-	
	ID AREA		
_			

FLASH MEMORY (NON-VOLATILE MEMORY)

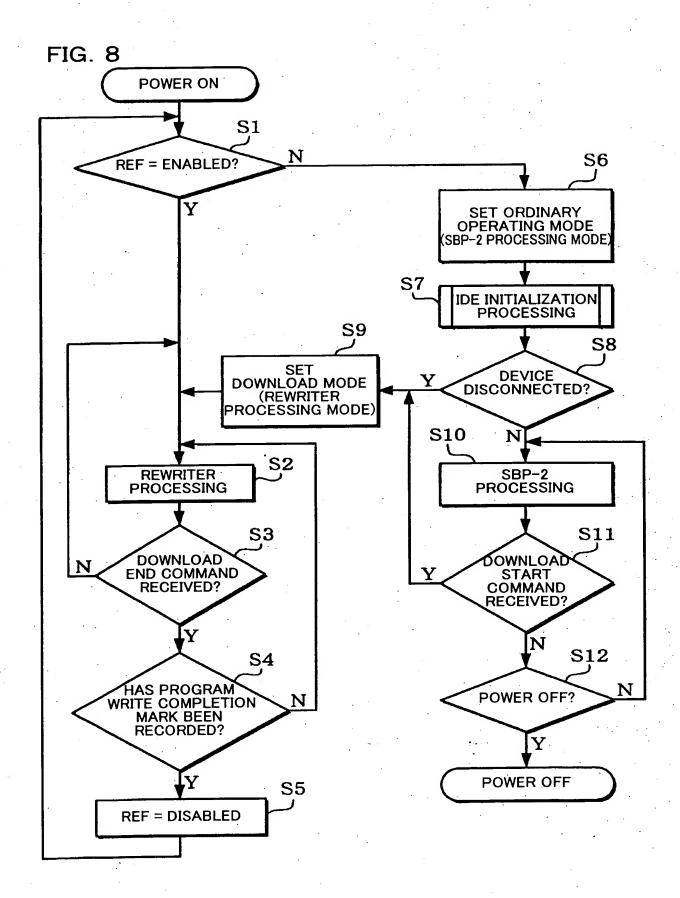


FIG. 9

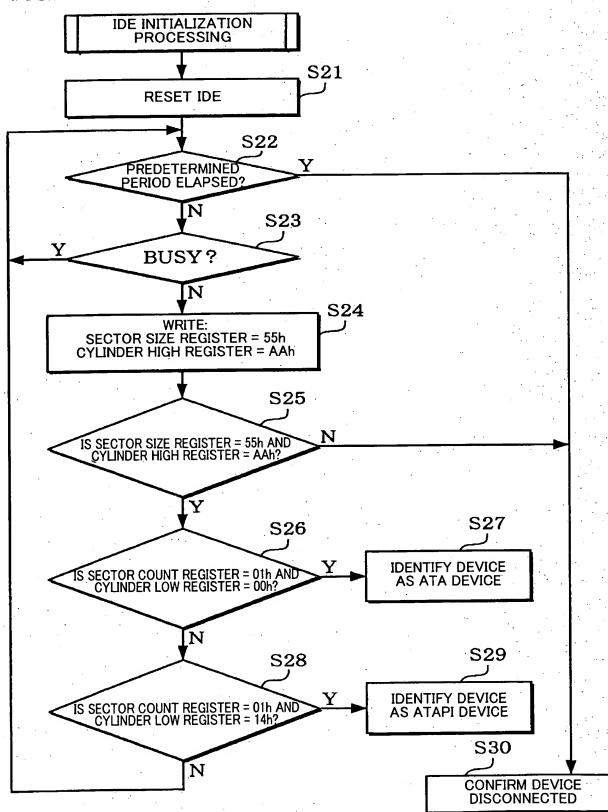


FIG. 10A

STATUS REGISTER

7	6	5	4	3	2	1	0
BSY	DRDY	#	#	DRQ	obs	obs	ERR

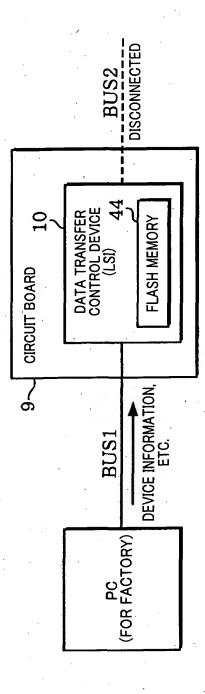
FIG. 10B ATA

REGISTER	
SECTOR COUNTER	01h
SECTOR NUMBER	01h
CYLINDER LOW	00h
CYLINDER HIGH	00h
DEVICE/HEAD	00h

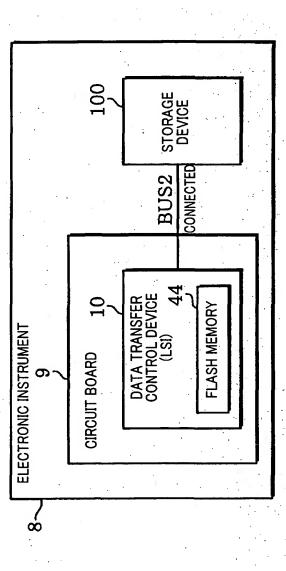
FIG. 10C ATAPI

REGISTER	
SECTOR COUNTER	01h
SECTOR NUMBER	01h
CYLINDER LOW	14h
CYLINDER HIGH	EBh
DEVICE/HEAD	00h OR 10h

FIG. 11A DURING FABRICATION (DOWNLOAD MODE)



DURING FABRICATION (INCORPORATION OF CIRCUIT BOARD) FIG. 11B



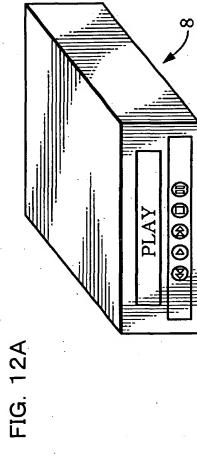


FIG. 12B DURING USE (ORDINARY OPERATING MODE)

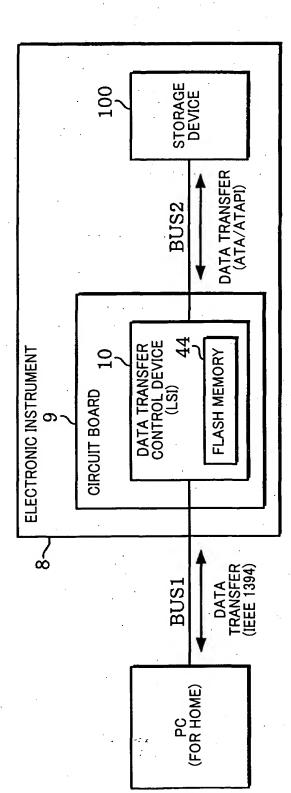


FIG. 13

DOWNLOADER HOST

REWRITER

INPUT ID TO BE WRITTEN

WRITE ID

WAIT FOR FLASH MEMORY WRITE TIME

READ STATUS (BUSY)

WRITE TO FLASH

STATUS (READY)

RETURN STATUS (READY)

RETURN ID

CONFIRM ID

(END)

ID DOWNLOAD

DOWNLOADER HOST

REWRITER

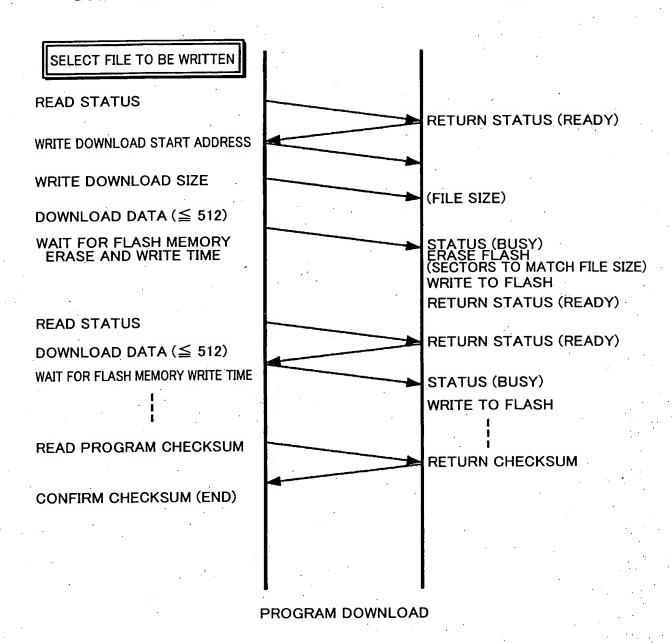
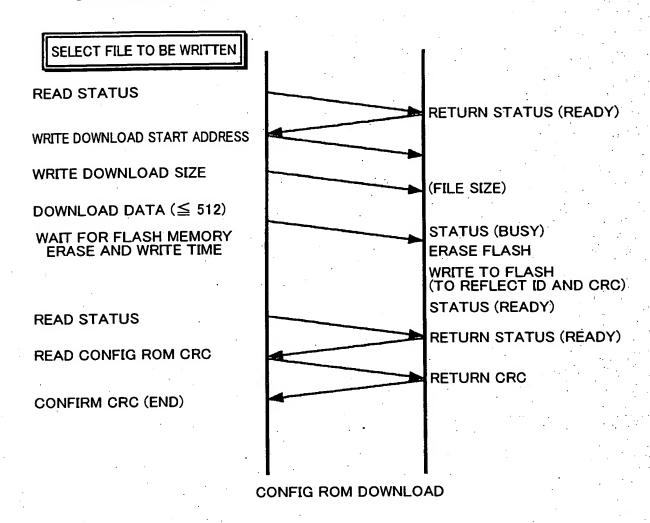
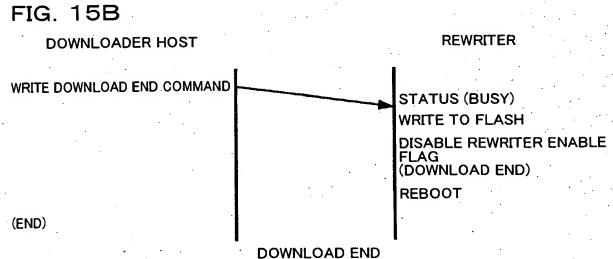


FIG. 15A

DOWNLOADER HOST

REWRITER





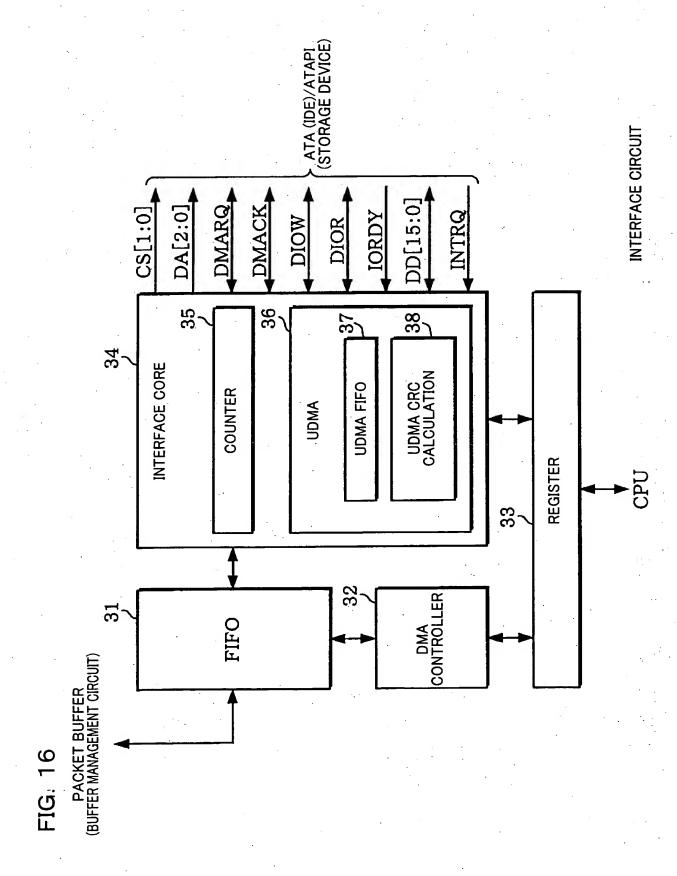


FIG. 17A
PIO READ (STORAGE DEVICE → DATA TRANSFER CONTROL DEVICE → PC)

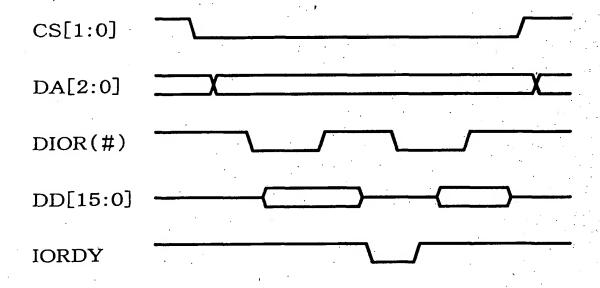


FIG. 17B
PIO WRITE (PC → DATA TRANSFER CONTROL DEVICE → STORAGE DEVICE)

